

PATENT APPLICATION

Sheet 1 of 1

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

200208966-1

APPLICATION NO.

CONFIRMATION NO.

APPLICANT

Andrew M. SPENCER

FILING DATE

Herewith

GROUP

REFERENCE DESIGNATION



U.S. PATENT DOCUMENTS

| EXAMINER INITIAL | DOCUMENT NUMBER | PUBLICATION DATE | NAME | Pages, Columns, Lines Where Relevant Passages or Figures Appear |
|---------------------|--------------------|---------------------|------|--|
| | 1A | | | |
| | 1B | | | |
| | 1C | | | |
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| | 1G | | | |
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| | 1K | | | |

FOREIGN PATENT DOCUMENTS

| | DOCUMENT NUMBER | PUBLICATION DATE | NAME OF PATENTEE OR APPLICANT | Pages/Columns/Lines Where Relevant Passages/Figures Appear | Check if Translation attached |
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| | 1L | | | | |
| | 1M | | | | |
| | 1N | | | | |
| | 1O | | | | |
| | 1P | | | | |

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

| | | |
|---|----|---|
|  | 1Q | "Double Data Rate (DDR) SDRAM"; 512Mb: x8, DDR 400 SDRAM Addendum, Micron Technology, Inc., 2003 (pp 1-8). |
|  | 1R | Andrew M. Spencer et al.; "Multiple Buffer Memory Interface," U.S. patent application number not yet assigned, filed June 27, 2003 (21 p.). |
| | 1S | |

EXAMINER

DATE CONSIDERED

06/13/02